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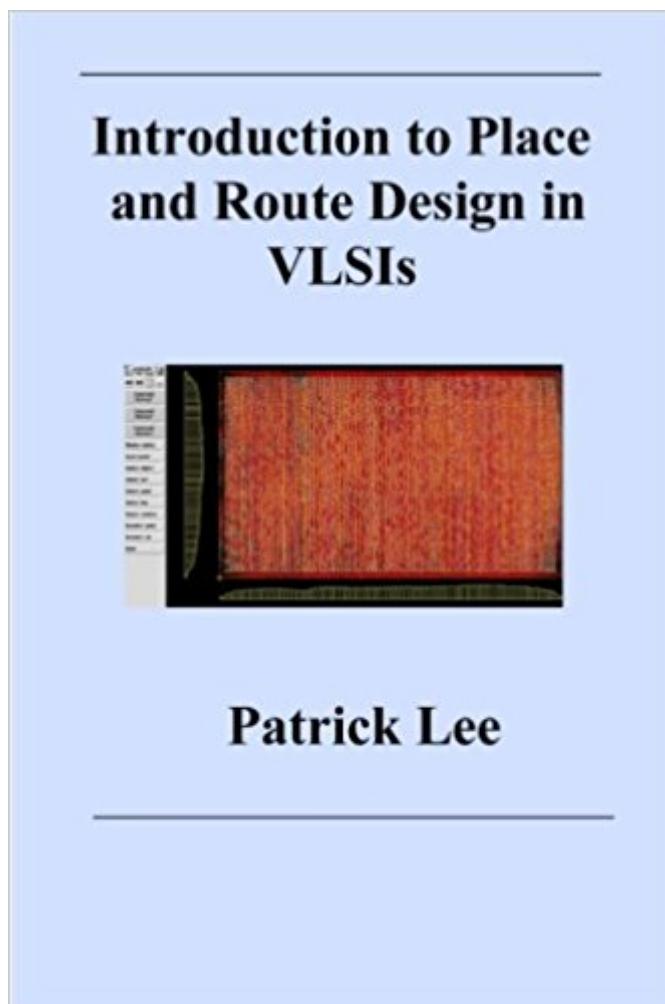
Introduction To Place And Route Design In VLSIs

Introduction to Place and Route Design in VLSIs

Patrick Lee

Synopsis

The book is organized in seven chapters. Physical design flow. Timing constraints. Place and route concepts. Tool vendors. Process constraints. Timing closure. Place and route methodology and flow. ECO and spare gates. Formal verification. Coupling noise. Chip optimization and tapeout.

Book Information

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Customer Reviews

This is a very good introductory book for someone not much on the place and route field. It explains the basic terminologies and flows in details. It is especially good for layout or logic designers or students trying to understand place and route concepts. It is an industry book, although some mathematical algorithms are explained for key technologies used place and route engines. References and figures also help in the book.

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